

**REMARKS**

**Status of the Application**

Applicant has amended claims 16, 20 and 27 to clarify that some operations in the baseband processing section are synchronized with a first clock and some operations in the information processing terminal are synchronized with a second clock. These amendments are supported throughout the specification and drawings. Applicant respectfully submits that no new matter has been added.

**Claims 1, 2, 9, 16-29, 39, 49 and 50 --- 35 U.S.C. § 103(a)**

Claims 1, 2, 9, 16-29, 39, 49 and 50 have been rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over JP 08-149035 to Imura Shigeru *et al.* ("Imura") in view of U.S. Pat. No. 7,050,779 to Ono *et al.* ("Ono"). Applicant traverses this rejection.

The combination of Imura and Ono does not disclose or suggest at least wherein said baseband processing section and said information processing terminal operate in synchronization with a clock, as recited in claim 1. The Examiner concedes that Imura does not disclose at least this feature and relies on Ono to allege such disclosure (Office Action, page 4). Ono, however, does not cure the deficiencies of Imura.

Ono is directed to a transmit/receive integrated circuit (Abstract). The integrated circuit includes a baseband processing section, and a transmit/receive section which receives clock signals for synchronization from the baseband circuit (column 6, lines 18-31). Ono does not, however, disclose or suggest the information processing terminal claimed by Applicant. Therefore, Ono cannot disclose that the baseband processing section and an information processing terminal operate in synchronization with a clock.

Accordingly, it would not be obvious to one of ordinary skill in the art at the time the invention was made to combine the references as attempted by the Examiner. Even if the references were combined, the combination would still not result in the features claimed by applicant.

Thus, claim 1 is patentable over the combination of Imura and Ono. Claims 17, 21, 24, 29, 39, 49 and 50 contained features similar to the features recited in claim 1 and are therefore patentable for similar reasons. Claims 2, 9, 18, 19, 22, 23, 25 and 26, which depend from one of claims 1, 17, 21 and 24, are patentable at least by virtue of their dependence.

With regards to claims 16, 20, 27 and 28, the Examiner concedes that Imura does not disclose or suggest at least an information processing terminal which converts the reception digital baseband signal into a reception data and converts a transmission data into a transmission digital baseband signal. The Examiner relies on Ono for alleging such disclosure.

Ono, however, discloses an integrated circuit which provides only analog RF-to-baseband conversion and analog baseband-to-RF conversion of the information signals. As disclosed by Ono, analog-to-digital conversion and digital-to-analog conversion is provided only for generating input offset voltages to the programmable gain amplifiers. See column 3, lines 54-60 and column 4, lines 25-47 of Ono. Further, Ono discloses a control register in the control circuit for the RF processing unit which accepts control signals from the baseband circuit (column 6, lines 18-31). The integrated circuit of Ono performs analog demodulation of received RF signals and outputs analog I and Q (in-phase and quadrature) to a baseband circuit. The inverse operations are performed for transmitted signals. However, Ono is silent as to the output of the baseband circuit to an information processing terminal.

Ono, therefore, does not provide analog-to-digital conversion and digital-to-analog conversion of the received and transmitted signals. Thus, Ono does not disclose or suggest at least an information processing terminal which converts the reception digital baseband signal into a reception data and converts a transmission data into a transmission digital baseband signal, as set forth in the claims. Accordingly, Ono does not cure the deficiencies of Imura.

In view of the above, claims 16, 20, 27 and 28 are patentable over the combination of Imura and Ono.

**Claims 3-8, 10-15, 30-38 and 40-48 --- 35 U.S.C. § 103(a)**

Claims 3-8, 10-15, 30-38 and 40-48 have been rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Imura in view of Ono, and further in view of U.S. Pat. Pub. No. 2003/0118081 to Philips *et al.* ("Philips"). Applicant traverses this rejection.

The combination of Imura, Ono and Philips does not disclose or suggest at least wherein said baseband processing section and said information processing terminal operate in synchronization with a clock, as incorporated into claims 3-8, 10-15, 30-38 and 40-48 by virtue of their dependence from claims 1, 29 and 39. As established above, the combination of Imura and Ono fails to disclose or suggest at least these features. Philips does not cure the deficiencies of the Imura-Ono combination.

Philips is directed to a programmable modem apparatus integrated circuit (Abstract). Philips discloses a receiver chain clock generator coupled to the receiver chain on the integrated circuit (page 4, paragraph [0046]). Philips, however, is silent as to any synchronization between the receiver section and any information processing terminal. Thus, even assuming *arguendo* that Philips teaches the features relied upon by the Examiner, Philips fails to disclose or suggest

clock synchronization between a baseband processing section and an information processing terminal. Therefore, it would not be obvious to one of ordinary skill in the art at the time the invention was made to combine the references as attempted by the Examiner since the combination would still not result in the features claimed by Applicant.

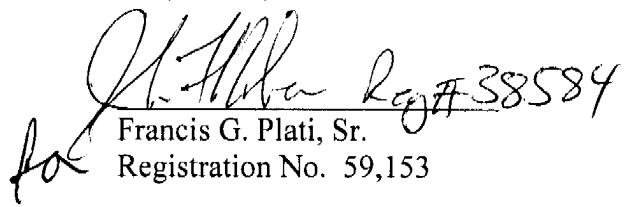
Accordingly claims 3-8, 10-15, 30-38 and 40-48 are patentable over the accommodation of Imura, Ono and Philips.

**Conclusion**

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

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Respectfully submitted,

  
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